

### **Applying FPGA partial reconfiguration for digital system simulation**

**Arhipov, Anton; Ellervee, Peeter** Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK kolmanda aastakonverentsi artiklite kogumik : 25.-26. aprill 2008, Voore külalistemaja 2008 / p. 145-148 : ill

### **Architectural exploration tasks for on-chip embedded systems**

**Reinsalu, Uljana; Arhipov, Anton; Ellervee, Peeter** BEC 2008 : 2008 International Biennial Baltic Electronics Conference : proceedings of the 11th Biennial Baltic Electronics Conference : Tallinn University of Technology : October 6-8, 2008, Tallinn, Estonia 2008 / p. 171-174 : ill

### **Clock manipulation for heterogeneous emulation environment**

**Ellervee, Peeter; Arhipov, Anton; Tammemäe, Kalle** Proceedings [of] 24th IEEE Norchip Conference : Linköping, Sweden, 20-21 November 2006 2006 / p. 213-216 : ill <https://ieeexplore.ieee.org/abstract/document/4126984>

### **FPGA based emulation environment**

**Jervan, Gert; Arhipov, Anton; Ellervee, Peeter** 2nd International Workshop on Reconfigurable Communication Centric System-on-Chip (ReCoSoC'06) 2006

### **HDL-s for students with different background**

**Reinsalu, Uljana; Arhipov, Anton; Evarson, Teet; Ellervee, Peeter** Proceedings MSE 2007 : 2007 IEEE International Conference on Microelectronic Systems Education : 3-4 June 2007, San Diego, CA 2007 / p. 69-70  
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### **Translating behavioral VHDL for emulation**

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### **Using soft-core processors and FPGA development boards for hardware emulation**

**Arhipov, Anton; Ellervee, Peeter** Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK teise aastakonverentsi artiklite kogumik : 11.-12. mai 2007, Viinistu kunstimuuseum 2007 / lk. 155-158 : ill