

FSM decomposition with application to FPGA synthesis

Sudnitsõn, Aleksander; Mihhailov, Dmitri; Kruus, Margus; Tarletski, Konstantin International Conference on Computer Systems and Technologies - CompSysTech'09 : Ruse, Bulgaria 2009 / p. IV.4.1-IV.4.6 <https://dl.acm.org/doi/10.1145/1731740.1731820>