

Bottlenecks in hardware design and design automation (Hardware synthesis: no pain, no gain)

Ellervee, Peeter CREDES Summer School : Dependable Systems Design : handouts 2011 / p. 49-58 : ill

Fault modeling and diagnosis in digital systems

Ubar, Raimund-Johannes CREDES Summer School : Dependable Systems Design : handouts 2011 / p. 91-106 : ill

Scalable dependable SoC architectures based on Networks-on-Chip

Hollstein, Thomas CREDES Summer School : Dependable Systems Design : handouts 2011 / p. 35-47 : ill

System level design for dependability and reliability

Jervan, Gert CREDES Summer School : Dependable Systems Design : handouts 2011 / p. 5-20 : ill

Techniques for automated localization and correction of design errors

Raik, Jaan CREDES Summer School : Dependable Systems Design : handouts 2011 / p. 107-118 : ill