

Address-based data processing over N-ary trees

Sklyarov, Valery; Skliarova, Ioulia; **Kruus, Margus; Mihailov, Dmitri; Sudnitsõn, Aleksander** EuroCon 2013 : 01-04 July 2013, Zagreb, Croatia 2013 / p. 1790-1797 : ill

Implementation in FPGA of address-based data sorting

Sklyarov, Valery; Skliarova, Ioulia; **Mihailov, Dmitri; Sudnitsõn, Aleksander** 21st International Conference on Field Programmable Logic and Applications : FPL 2011 : Chania, Crete, Greece, 5-7 September 2011 2011 / p. 405-410 : ill

Implementation of sorting algorithms in reconfigurable hardware

Skliarova, Ioulia; Sklyarov, Valery; **Mihailov, Dmitri; Sudnitsõn, Aleksander** 2012 IEEE Mediterranean Electrotechnical Conference (MELECON 2012) : Yasmine Hammamet, Tunisia, March 25-28, 2012 2012 / p. 107-110 : ill

Processing N-ary trees in hardware circuits

Sklyarov, Valery; Skliarova, Ioulia; **Mihailov, Dmitri; Sudnitsõn, Aleksander** 13th International Symposium on Integrated Circuits (ISIC) : Singapore, 12-14 December 2011 : proceedings 2011 / p. 262-265 : ill