

### **Address-based data processing over N-ary trees**

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### **Implementation in FPGA of address-based data sorting**

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[https://www.researchgate.net/publication/220760392\\_Implementation\\_in\\_FPGA\\_of\\_Address-Based\\_Data\\_Sorting](https://www.researchgate.net/publication/220760392_Implementation_in_FPGA_of_Address-Based_Data_Sorting)

### **Implementation of sorting algorithms in reconfigurable hardware**

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### **Processing N-ary trees in hardware circuits**

Sklyarov, Valery; Skliarova, Iouliia; **Mihhailov, Dmitri; Sudnitsõn, Aleksander** 13th International Symposium on Integrated Circuits (ISIC) : Singapore, 12-14 December 2011 : proceedings 2011 / p. 262-265 : ill <https://ieeexplore.ieee.org/document/6131946>