

An SVM scheme for three-level quasi-switched boost T-type inverter with Enhanced voltage gain and capacitor voltage balance

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A fast current harmonic detection and mitigation strategy for shunt active filter

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Inductor Current Ripple Analysis and Reduction for Quasi-Z-Source Inverters with An Improved ZSVM6 Strategy

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A simple space vector modulation method with DC-link voltage balancing and reduced common-mode voltage strategy for a three-level T-type quasi-Z source inverter

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A Toolbox to design inverters for automotive applications

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